

IN THE CLAIMS:

Please cancel claims 1-33.

34. (Currently amended) A transmitter system, comprising:

first signal circuitry coupled to first signal transmission lines for transmitting a first set of signals on the first signal transmission lines; and

first reference circuitry coupled to first reference transmission lines for transmitting a first pair of complementary oscillating voltage references on the first reference transmission lines, the first pair of complementary oscillating voltage references for comparison against the first set of signals by a receiver coupled to the first signal transmission lines and to the first reference transmission lines, the comparison enabling level conversion of the first set of signals.

35. (Currently Amended) A transmitter system, comprising:

first signal circuitry coupled to first signal transmission lines for transmitting a first set of signals on the first signal transmission lines; and

first reference circuitry coupled to first reference transmission lines for transmitting a first pair of complementary oscillating voltage references on the first reference transmission lines, the first pair of complementary oscillating voltage references for comparison against the first set of signals by a receiver coupled to the first signal transmission lines and to the first reference transmission lines, the comparison enabling level conversion of the first set of signals ~~The transmitter system of claim 1,~~ wherein each signal of the first set of signals has a signal voltage swing and wherein each reference of the first pair of references has a reference voltage swing equal to or less than the signal voltage swing.

36. (Currently amended) The transmitter system of claim 34 ~~1~~, wherein at least one signal of the first set of signals is transmitted earlier than the first pair of references.

37. (Currently amended) The transmitter system of claim 34 1, wherein the ~~first signal-circuitry includes~~ signal transmission lines for half of the first set signals are disposed on one side of the first reference transmission lines ~~first reference circuitry~~ and the signal transmission lines for the other half are disposed on the other side of the first reference transmission lines ~~first reference circuitry~~.

38. (Currently amended) The transmitter system of claim 34 1, further comprising
second signal circuitry coupled to second signal transmission lines for transmitting a second set of signals on the second signal transmission lines; and
second reference circuitry coupled to second reference transmission lines for transmitting a second pair of complementary oscillating voltage references on the second reference transmission lines, the second pair of complementary oscillating voltage references for comparison against the second set of signals by the receiver, the comparison enabling level conversion of the second set of signals.

39. (Currently amended) The transmitter system of claim 38 5, wherein the first signal circuitry, the first reference circuitry, the second signal circuitry and the second reference circuitry are all disposed on a single chip.

40. (Currently amended) The transmitter system of claim 34 1, wherein at least one signal of the first set of the signals has a bit-to-bit skew greater than zero relative to the first pair of references.

41. (Currently amended) The transmitter system of claim 40 7, wherein the oscillation of the pair of oscillating voltage references enables a receiver to effect level conversion of the at least one signal despite the skew.

42. (Currently amended) The transmitter system of claim 34 1, wherein each reference of the pair of references is compared against each signal of the first set of signals by a receiver to effect the level conversion.

43. (Currently amended) The transmitter system of claim 34 ~~1~~, further comprising circuitry for transmitting a clock signal.